IN THE SPECIFICATION

Please amend the paragraph on page 5, line 19, as follows:

-- The switched node of FIG. 2 is interconnected with a plurality of other switched nodes 20 such as shown in FIG. 3 to form a multi-dimensional switched fabric. Each of the switched nodes in FIG. 3 comprises four bi-directional ports (North, East, South and West) forming a two-dimensional fabric. Control data 22 is generated by a switched fabric microprocessor, such as the microprocessor 24 in the switched fabric network 26 shown in FIG. 4. In one embodiment, the network data transmitted through the switched nodes 20 consist of packets having a packet header comprising routing data which identifies the ~~source~~ destination node for the packet. The packets are buffered in buffers 28_0-28_N, and the microprocessor 24 processes the packet header in order to route the packet through the switched nodes 20. A suitable routing algorithm implemented by the microprocessor 24 of FIG. 4 generates control data 22 for configuring the switched nodes 20. Any suitable routing algorithm may be employed, and it may support Unicast, Multicast, or Broadcast delivery mechanisms. The routing decisions may be made centrally, at the source, distributed, or multiphase, implemented using a lookup table or using a finite-state machine. Further, the routing algorithm may be deterministic or adaptive. A discussion of various routing algorithms which may be employed in the embodiments of the present invention is provided by Jose Duato et al. in the text book "Interconnection Networks, an Engineering Approach", IEEE Computer Society, 1997. --